

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yi Ding
Assignee: ProMOS Technologies, Inc.
Title: Fabrication Of Gate Dielectric In Nonvolatile Memories In Which A
Memory Cell Has Multiple Floating Gates
Application No.: 10/632,154 Filing Date: July 30, 2003
Examiner: Nguyen, Khiem D. Group Art Unit: 2823
Docket No.: M-15230 US

San Jose, California
November 3, 2004

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR § 1.97(c)

Dear Sir:

Pursuant to 37 CFR § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying form PTO-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are enclosed except for United States Patents and United States Published Patent Applications.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
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I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on November 3, 2004.

Michael Shenker 11-3-04
Attorney for Applicant Date of Signature

Respectfully submitted,

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U.S. Department of Commerce, Patent and Trademark Office				Atty Docket No.		Serial No.	
				M-15230 US		10/632,154	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant			
(Use several sheets if necessary)				Yi Ding			
				Filing Date		Group	
				July 30, 2003		2823	
U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,232,185	May-01	Wang			
	AB	5,912,843	Jun. 1999	Jeng			
	AC	6,436,764	Aug. 2002	Hsieh			
	AD	6,344,993	5 Feb. 2002	Harari et al.			
	AE	5,668,757	Sept. 1997	Jeng			
	AF	6,355,524	Mar. 2002	Tuan et al.			
	AG	6,696,340	Feb. 2004	Furuhata			
	AH	5,705,415	Jan. 1998	Orlowski et al.			
	AI						
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